

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|---|------------------|---------|------------------|
| L1 | 49 | 714/43.ccls. and @pd>"20040701" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 11:13 |
| L2 | 577 | 714/43.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 11:12 |
| L3 | 51 | 714/43.ccls. and @pd>"20040701" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 11:13 |
| L4 | 12 | 714/715.ccls. and @pd>"20031211" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:18 |
| L5 | 0 | 714/715.ccls. and ((linear adj feedback adj shift adj register) (lfsr)) and @pd>"20031211" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:19 |
| L6 | 6 | 714/716.ccls. and @pd>"20040408" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:20 |
| L7 | 0 | 714/716.ccls. and ((linear adj feedback adj shift adj register) (lfsr)) and @pd>"20040408" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:21 |
| L8 | 6 | 714/739.ccls. and ((linear adj feedback adj shift adj register) (lfsr)) and @pd>"20040617" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:21 |
| L9 | 4 | 714/717.ccls. and @pd>"20030508" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:24 |

| | | | | | | |
|-----|------|---|---|----|-----|------------------|
| L10 | 6 | 714/41.ccls. and @pd>="20040422" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:25 |
| L11 | 4 | 714/738.ccls. and ((linear adj feedback adj shift adj register) (lfsr)) and @pd>="20040506" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:26 |
| S1 | 1 | 714/43.ccls. and (linear adj feedback adj shift adj register) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/07/07 15:29 |
| S2 | 601 | "714"/\$.ccls. and (linear adj feedback adj shift adj register) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/07/08 09:30 |
| S3 | 59 | "714"/\$.ccls. and ((linear adj feedback adj shift adj register) (lfsr)) and ((bus path) adj (test\$4)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/07/07 15:56 |
| S4 | 527 | 714/43.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 11:12 |
| S5 | 1141 | 714/738.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/07/07 16:17 |
| S6 | 176 | 714/715.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:18 |
| S7 | 8 | 714/715.ccls. and ((linear adj feedback adj shift adj register) (lfsr)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:19 |
| S8 | 144 | 714/716.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:20 |

| | | | | | | |
|-----|-----|---|---|----|-----|------------------|
| S9 | 2 | 714/716.ccls. and ((linear adj feedback adj shift adj register) (lfsr)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:21 |
| S10 | 47 | 714/739.ccls. and ((linear adj feedback adj shift adj register) (lfsr)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:21 |
| S11 | 82 | 714/717.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:24 |
| S12 | 80 | 714/41.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:25 |
| S13 | 139 | 714/739.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/07/08 08:40 |
| S14 | 45 | 714/738.ccls. and ((linear adj feedback adj shift adj register) (lfsr)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/11/15 12:26 |
| S15 | 0 | "714"/\$.ccls. and (linear adj feedback adj shift adj register) and (ahb) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/07/08 09:30 |
| S16 | 0 | "714"/\$.ccls. and (linear adj feedback adj shift adj register) and (advanced adj high adj performance adj bus) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/07/08 09:31 |
| S17 | 1 | (linear adj feedback adj shift adj register) and (advanced adj high adj performance adj bus) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/07/08 09:31 |
| S18 | 1 | ((linear adj feedback adj shift adj register) (lfsr)) and (advanced adj high adj performance adj bus) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/07/08 09:31 |

| | | | | | | |
|-----|----|--|---|----|-----|------------------|
| S19 | 78 | advanced adj high adj performance adj bus | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2004/07/08 14:18 |
|-----|----|--|---|----|-----|------------------|